

# Voltage Sag Compensation using SMES Based DVR Technology & Pi Controller

Shruti Verma<sup>1</sup>, Amit Agrawal<sup>2</sup>, Dr. Dharmendra Kumar Singh<sup>3</sup>

1M.Tech Scholar, 2Assistant Professor E.E department, 3 Head of department EEE Dept, CV Raman University,  
Bilaspur.Chhattisgarh, India

**Abstract-** Power quality is one of the most important topic that electrical engineering have been noticed in recent years. This paper describes the super conducting magnetic energy storage SMES based dynamic voltage restorer protect consumers from the grid voltage transients. A fine solution to power quality and optimization related concerned issues can be easily provided through DVR..Voltage sag is the most prominent problem as paer the P-Q issue. The voltage sag is mitigated and real power is restored and thus wastage is overcome, providing protection to consumers from grid voltage fluctuations by the Superconducting magnetic energy storage (SMES) technology based DVR. Power flow stabilization and control in the transmission network level are some of the increasing interest in application of Superconducting magnetic energy storage (SMES) systems. The properties and characteristics of high energy density and quick response incorporates a superconducting magnet as the energy storage unit to improve the compensation capability of DVR. This paper analyses the operation principle of the SMES based DVR, and designs the DVR output voltage control method using MATLAB SIMULINK, the models of the SMES based DVR is established, and the simulation tests are performed to evaluate the system performance.

**Keywords-**Matlab/Simulink, SMES, PCS, DVR,PWM, IGBT

\*\*\*\*\*

## 1. INTRODUCTION

Power quality is one of the most important topic that electrical engineering have been noticed in recent years. The voltage sag ,swell, interruptions & flicker fluctuations etc. causes voltage waveforms to be disturbed from ideal waveform and also due to the use of non-linear loads. Such voltage distortion adversely affects the performance of equipment connected in the system. Voltage sag is one of the problems related to power quality. This phenomenon happen continuously in transmission and distribution systems. Power quality problems, such as voltage sag which arise due to a fault or a pulsed load, can cause interruption on critical load. [1][5]The power disruption on the load can be avoided by installing DVR on sensitive load, restores the line voltage to its nominal value within the response time of a few milliseconds. Modern Pulse-Width Modulated (PWM) inverters capable of generating accurate high quality voltage waveforms form the power electronic heart of the new Custom Power devices. [2].

Active power will be supplied through DVR with help of SMES and required reactive power is generated internally without any means SMES storage. DVR can compensate voltage at both transmission and distribution sides. Usually a DVR is installed on a critical load feeder. During the normal operating condition (without sag condition) DVR operates in a low loss standby mode [2]. During this condition the DVR is said to be in steady state. When a disturbance occurs (abnormal condition) and supply voltage deviates from nominal value, DVR supplies voltage for compensation of sag and is said to be in transient state. The DVR is

connected in series between the load and the supply voltage [3].

Dynamic voltage restorer can provide the most cost effective solution to mitigate voltage sags by establishing the proper voltage quality level that is required by customer. [4][5][6].Voltage sags are characterized by a reduction in voltage, but the load is still connected to the supply. [6][21].A large power system is subjected to various disturbances very changes in operating conditions cause's oscillations and electromagnetic torque in the system. The system variables such as frequency, load angle, active and reactive power of generator should be maintained within permissible limits and oscillations should be damped out as quickly as possible for a secured operation of a power system .SMES is very effective to improving dynamic performance of power system due to its large energy storage capacity, fast response and negligible losses. [7][8][14][15][16]

Out of the various voltage disturbances, voltage sag is a frequent disturbance in power system. 92% of the interruptions in industrial installations are due to voltage sags. So in order to overcome this deficiency SMES based DVR has been used to improve the performance of power system by improving voltage profile as it is having high power rating with maximum efficiency then any other energy storage devices. In this paper proposes a super conducting magnetic energy storage unit, as the energy storage unit of DVR.[9][10]

## 2. MODELLING & METHODOLOGY OF PROPOSED SMES BASED DVR –

### 2.1 CONFIGURATION OF DVR-

The DVR can be used for protection and recovery or restore the quality of voltage to the sensitive load. Dynamic Voltage Restorer (DVR) is one of the effective custom power devices that can be used to improve power quality from any disturbances in the distribution line. A set of three phase voltages with an appropriate magnitude and duration can be injected through injection transformer and must be in phase with the grid voltage. A DVR is a solid state power electronics switching device consisting of either GTO or IGBT, a capacitor bank as an energy storage device and injection transformers.

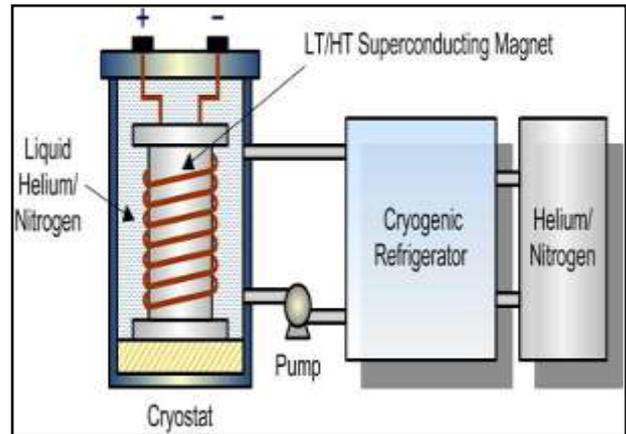


Fig 2.2.1- SMES schematic model

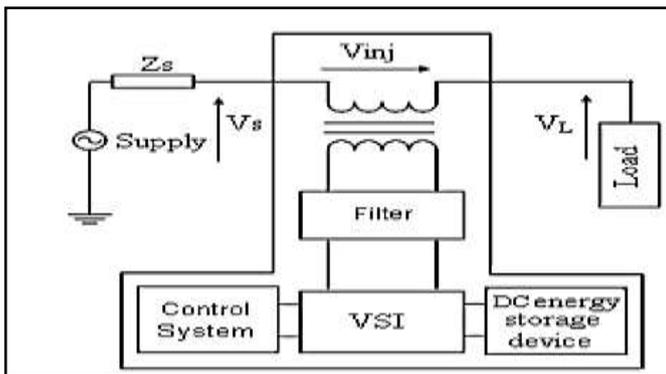


Fig 2.1- Schematic representation of a DVR

### 2.2 SMES AS ENERGY STORAGE DEVICE OF DVR

SMES is the Super Conducting Magnetic Energy System used in DVR as energy storage device. SMES systems have very fast charge and discharge times which make them an attractive energy storage system for sag mitigation. Another advantage of SMES systems is the very low losses due to the superconducting characteristics. It consists of superconducting magnetic energy storage unit, capacitor bank, VSI, low pass filter. It consists of main system and its sub systems. An SMES unit includes of a large superconducting coil, whose temperature is maintained below the cryogenic temperature by a cryostat or Dewar that contains either helium or nitrogen liquid as the coolant.[16][17] During standby condition, to reduce the energy losses a bypass switch is used[18][19]. Finally a transformer which provides the power system connection and co-ordination and PCS operating voltage will reduce to acceptable levels.

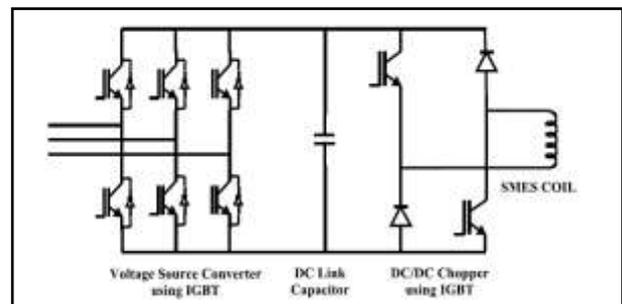


Fig 2.2.2 VSC Based SMES model

The structural figure 2.2.2 of the SMES unit with VSC-based PCS includes of a star-delta transformer, a basic six-pulse PWM converter with insulated gate bipolar transistor (IGBT) as the switching device, a two quadrant bidirectional dc-dc chopper using IGBT, and an inductor as the superconducting coil. The decoupling of ac/dc converter and the dc-dc bidirectional converter is obtained by a large dc link capacitor. A power electronic link between the ac supply network and the dc current controlled superconducting coil is established by the PWM VSC. The PWM signal is obtained for the switching of IGBT by comparing the reference signal obtained from abc conversion with the high frequency triangular carrier signal. Throughout the operation the dc voltage across the capacitor is kept at its reference value by the six-pulse PWM converter. In our model VSC based SMES is used.[12].

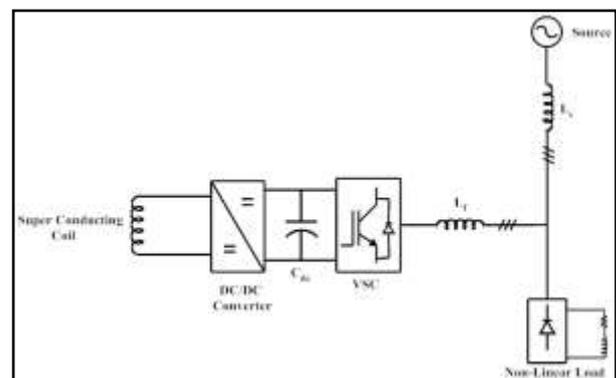


Fig:2.2.3 Proposed System Topology

Here, the PCS consists of a VSC and A DC/DC converter to control the source current as well as the charge-discharge cycle of SMES. DC/DC converter is a simple voltage bidirectional converter consists of IGBTs and diodes . When the switches are ‘ON’ SMES coil gets charged and positive voltage is applied on it; when they are ‘OFF’ negative voltage is applied on it and it discharges through diode. In both modes current remains unidirectional. During standby condition one of the switches is ‘ON’ and current circulates between that switch and one diode. The switching of this is regulated to get a constant dc-link voltage. Here, VSC is a six-pulse conventional full bridge converter. IGBT anti-parallel with a diode is used as the switch to get bidirectional current. It is controlled to operate in both rectifying and inverting modes.[20][21]

A voltage bidirectional dc-dc chopper is used to regulate the charge-discharge of the superconducting coil. The dc-dc chopper is controlled to make the voltage across SMES coil such as positive (IGBTs are switched ON) or negative (IGBTs are switched OFF) and then the energy reserved in SMES can be supplied or consumed accordingly. Hence, the charging and discharging of superconducting coil depends on the average voltage per cycle across the coil that is calculated by the duty cycle of the two-quadrant chopper. In order to obtain the PWM gate pulses for the IGBTs of the dc/dc converter, the estimated signal is compared with the triangular/saw-toothed carrier signal .[21].

The applied voltage across SMES coil is controlled by regulating the conduction time of the IGBT over the switching cycle. While the duty cycle is 0.5, average voltage across the SMES coil and average DC current in the VSC are both zero, and net power transferred throughout one complete switching cycle is zero. For a duty cycle larger than 0.5, the coil gets charged; while at less than 0.5, the coil gets discharged. Therefore, the control of charge/discharge is established by regulating the duty cycle of the switching devices.

### 2.3 PRINCIPLE OF OPERATION OF SMES

**Principle of Operation-**The systems works on the principle of energy balance between sources, load and SMES coil. The source current can be described as

$$I_s = I_l \pm I_i \quad (i)$$

Where  $I_s$ = Source Current  
 $I_l$ = Load Current  
 $I_i$ = Inverter Current

The output current of inverter is

$$I_i = f(I_o, D, M) \quad (ii)$$

Where  $I_o$ = SMES coil current  
 $D$  = Duty cycle of dc/dc converter  
 $M$  = Modulation index of inverter

Under load leveling condition the source current charges the coil when load power is less than source power. When, there is an increase in load occurs source power increases with load to its maximum and coil discharges to make energy balance between source, load and SMES.[21] In the whole operation dc link voltage is kept constant.

### 2.4 CONCEPT OF COMPENSATION TECHNIQUES

**IN DVR** -Voltage injection or compensation methods by means of a DVR depend upon the limiting factors such as DVR power ratings, various conditions of load, and different types of voltage sags. Some loads are sensitive towards phase angle jump and some are sensitive towards change in magnitude and others are tolerant to these. Therefore the control strategies depend upon the type of load characteristics. [8][11][27][28][34].

There are four different methods of DVR voltage injection which are as follows:

1. Pre-sag compensation method
2. In-phase compensation method
3. In-phase advanced compensation (IPAC) method
4. Voltage tolerance method with minimum energy injection

**1. Pre-sag/dip compensation method** The pre-sag method tracks the supply voltage continuously and if it detects any disturbances in supply voltage then it will inject the difference voltage between the sag or voltage at PCC and pre-fault condition, so that the load voltage can be restored back to the pre-fault condition. [8][9][10]

Compensation of voltage sags in the both phase angle and amplitude sensitive loads would be achieved by pre-sag compensation method. In this method, the injected active power cannot be controlled and it is determined by external conditions such as the type of faults and load conditions.[8][10][11]

$$VDVR = V_{prefault} - V_{sag} \dots (i)$$

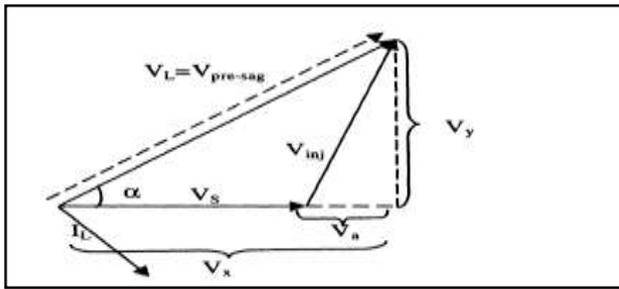


Figure 2.4.1: Pre sag compensation.

**2. In-phase compensation method** - In this method, the injected voltage is in phase with the supply side voltage irrespective of the load current and pre-fault voltage[8][10][22]. The phase angles of the pre-sag and load voltage are different but the most important criteria for power quality that is the constant magnitude of load voltage are satisfied. [8][10]

When the source voltage drops to voltage sag then the voltage source inverter (VSI) injects the missing voltage based on magnitude of the voltage drop. One of the advantages of this method is that the amplitude of DVR injection voltage is minimum for certain voltage sag in comparison with other strategies. Practical application of this method is in non-sensitive loads to phase angle jump. In our model in phase compensation method used.

change only the phase of the sag voltage. IPAC method uses only reactive power and, not all the sags can be mitigated without real power, as a consequence, this method is only suitable for a limited range of sags. [8]

**4. Voltage tolerance method with minimum energy injection** -A small drop in voltage and small jump in phase angle can be tolerated by the load itself. If the voltage magnitude lies between 90%-110% of nominal voltage and 5%-10% of nominal state that will not disturb the operation characteristics of loads. Both magnitude and phase are the control parameter for this method which can be achieved by small energy injection.[8][9][10][32]

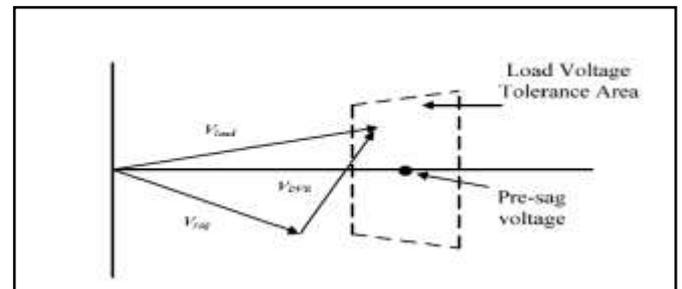


Figure 2.4.3: Voltage tolerance method with minimum energy injection

**2.5 SCHEMATICS & WORKING OF DVR**-The basic functions of a controller in a DVR are the detection of voltage sag/swell events in the system; computation of the correcting voltage, generation of trigger pulses to the PWM based DC-AC inverter, correction of any anomalies in the series voltage injection and termination of the trigger pulses when the event has passed[4][5][6][7].

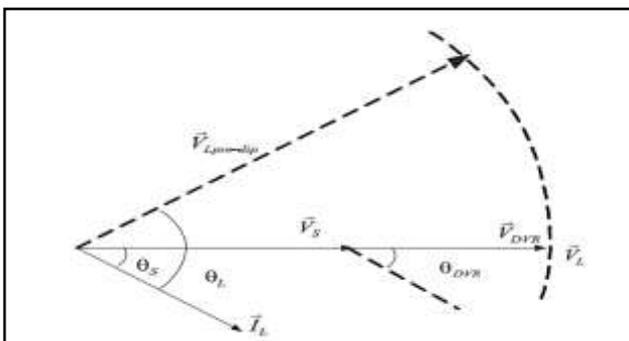


Figure 2.4.2 : In-phase compensation method

**3. In-phase advanced compensation (IPAC) method** -In this method, the real power spent by the DVR is decreased by minimizing the power angle between the sag voltage and load current. In case of pre-sag and in-phase compensation method the active power is injected into the system during disturbances.[31][32].

The minimization of injected energy is achieved by making the active power component zero by having the injection voltage phasor perpendicular to the load current phasor.[34][36][40][8] In this method, the values of load current and voltage are fixed in the system. So we can

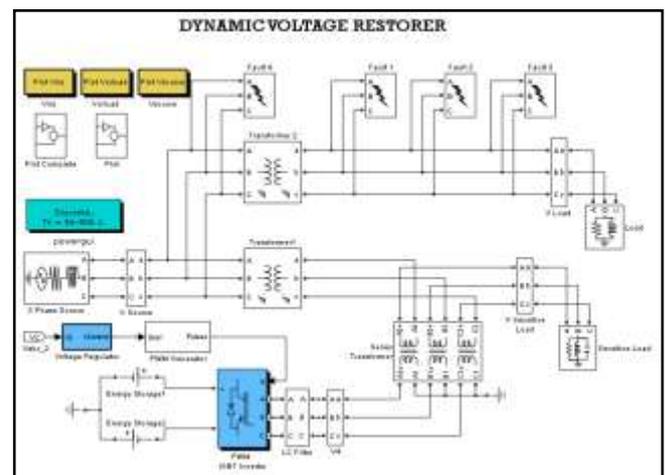


Figure 2.5.1- DVR various Blocks & its working

Figure 2.5- represents Dynamic voltage restorer with various blocks & working [10]. It represents a Voltage

sag is created at load terminals by three phase faults. Load voltage is converted into per unit quantity. Then the magnitude is compared with the reference voltage through which error signal is fed to PI controller.[22][23][7] The voltage is then fed to triggering circuit PWM ( pulse width modulated) control technique along with subsystem voltage regulator is applied for inverter switching so as to produce three phase sinusoidal voltage at the load terminals. The PI controller processes the error signal and generates the required angle  $\delta$  to drive the error to zero PI controller input is an actuating signal which is a difference between  $V_{ref}$  and  $V_{in}$ . The controller output compared at PWM signal generator results in desired firing sequence.[10][21]

The basic functions of a controller in a DVR are the detection of voltage sag/swell events in the system; computation of the correcting voltage, generation of trigger pulses to the PWM based DC-AC inverter, correction of any anomalies in the series voltage injection and termination of the trigger pulses when the event has passed. The controller may also be used to shift the DC-AC inverter into rectifier mode to charge the capacitors in the DC energy link in the absence of voltage sags/swells. [8][10][11].

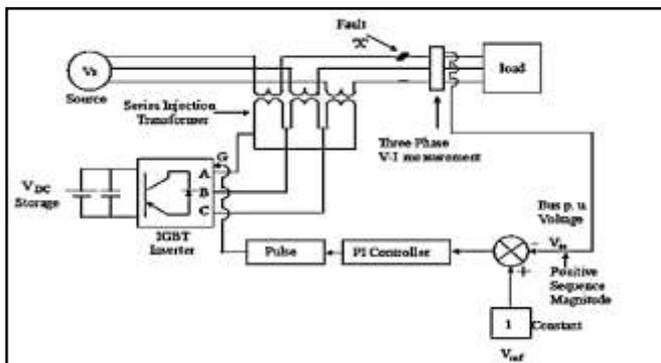


Fig 2.5.2- Simplified PI model of DVR

Fig 2.5.2- represents the simplified mode of DVR describing the working and circuit model of DVR.[11].The dqo transformation or Park's transformation is used to control of DVR. The dqo method gives the sag depth and phase shift information with start and end times. The quantities are expressed as the instantaneous space vectors. Firstly convert the voltage from a-b-c reference frame to d-q-o reference. For simplicity zero phase sequence components is ignored. The detection is carried out in each of the three phases. the measured terminal voltage ( $V_a, V_b, V_c$ )[10][20][22][8].The voltage sags is detected when the supply drops below 90% of the reference value whereas voltage swells is detected when supply voltage increases up to 25% of the reference value.[1][2][11].The error signal is used as a modulation signal that allows to generate a commutation pattern for the

power switches (IGBT's) constituting the voltage source converter. The commutation pattern is generated by means of the pulse width modulation technique (PWM); voltages are controlled through the Modulation The controller output when compared at PWM signal generator results in the desired firing sequence.[4][5] The sinusoidal voltage  $V$  control is phase-modulated by means of the angle  $\delta$  or delta as shown in figure 4.2 and the modulated three-phase voltages are given by

$$V_A = \sin(\omega t + \delta) \quad (iii)$$

$$V_B = \sin(\omega t + \delta - 2\pi/3) \quad (iv)$$

$$V_C = \sin(\omega t + \delta + 2\pi/3) \quad (v)$$

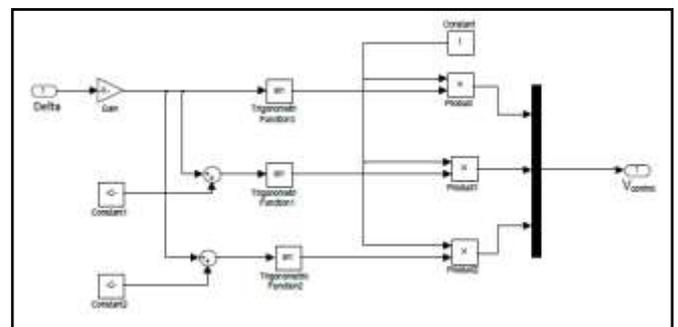


Fig 2.5.3 Phase-Modulation of the control angle  $\delta$  (Subsystem 1)

Fig 2.5.3 represents the alpha beta phase modulation of subsystem[5]. The modulated angle is applied to the PWM generators in phase A. The angles for phases B and C are shifted by  $120^\circ$  and  $240^\circ$ . In this PI controller, only voltage magnitude is taken as a feedback parameter in the control scheme.[5][8][11].

## 2.6 CONTROL SECTION

Fig 2.5.2 represents a model is simulated by injecting a three phase voltage on a three phase source voltage. Load voltage is converted into per unit quantity . Then the magnitude is compared with the reference voltage through which error signal is fed to PI controller. The voltage is then fed to triggering circuit PWM ( pulse width modulated ) control technique along with subsystem voltage regulator is applied for inverter switching so as to produce three phase sinusoidal voltage at the load terminals. The PI controller has fault processes the error signal and generates the required angle  $\delta$  to drive the error to zero PI controller input is an actuating signal which is a difference between  $V_{ref}$  and  $V_{in}$ . The controller output compared at PWM signal generator results in desired firing sequence.

The firing of the SMES coil in a VSI inverter of SMES coil during voltage sag through PWM generator having phase angle 0 degree, 120 degree and 240 degree for a set of 6 pulse thyristor. At a time any two thyristor conducts with firing angle 60 degree. At a time Pair of thyristor, T1,T6 or T2,T4 Or T3,T5 conducts in a universal bridge. The controller may also be used to shift the DC-AC inverter into rectifier mode to charge the capacitors in the DC energy link in the absence of voltage sags/swells. DC to dc link voltage is held constant through DC-DC chopper in SMES coil.

The dqo transformation or Park's transformation is used to control of DVR. The dqo method gives the sag depth and phase shift information with start and end times. The quantities are expressed as the instantaneous space vectors. Firstly convert the voltage from a-b-c reference frame to d-q-o reference. And then to alpha beta in the voltage regulator. For simplicity zero phase sequence components is ignored. The detection is carried out in each of the three phases. the measured terminal voltage.

The voltage sags is detected when the supply drops below 90% of the reference value whereas voltage swells is detected when supply voltage increases up to 25% of the reference value. The error signal is used as a modulation signal that allows to generate a commutation pattern for the power switches (IGBT's) constituting the voltage source converter. The commutation pattern is generated by means of the pulse width modulation technique (PWM); voltages are controlled through the Modulation The controller output when compared at PWM signal generator results in the desired firing sequence.

## 2.7 SIMULINK MODEL OF THE SMES BASED DVR TEST SYSTEM

### 2.7.1 SIMULATION PARAMETER

Here simulations are performed on the DVR test system using MATLAB/SIMULINK as shown in detailed figure 2.7.2. The system performance is analysed for compensate the load voltage in distribution networks under SLG fault condition.

Table 1- Simulation Parameters

Sr.No	System Quantity	Specification
1	PI Controller	$K_p = 0.5, K_i = 50$
2	Inverter Specification	IGBT based, 3 arms, 6 pulse, Carrier frequency = 1080 Hz. continuous simulation
3	Capacitance	10 micro faraday
4	SMES inductor	1 Henry
5	SMES coil charging time & Discharging time	0.3 sec & 0.8 seconds
6	System voltage	22 KV (line to line)
7	System fault-three phase fault	0.01ohms (for 0.2-0.7seconds transition time)
8	Load	10 kw RLC Load
9	Injection Transformer	22 KV /10 KV, 10 MVA

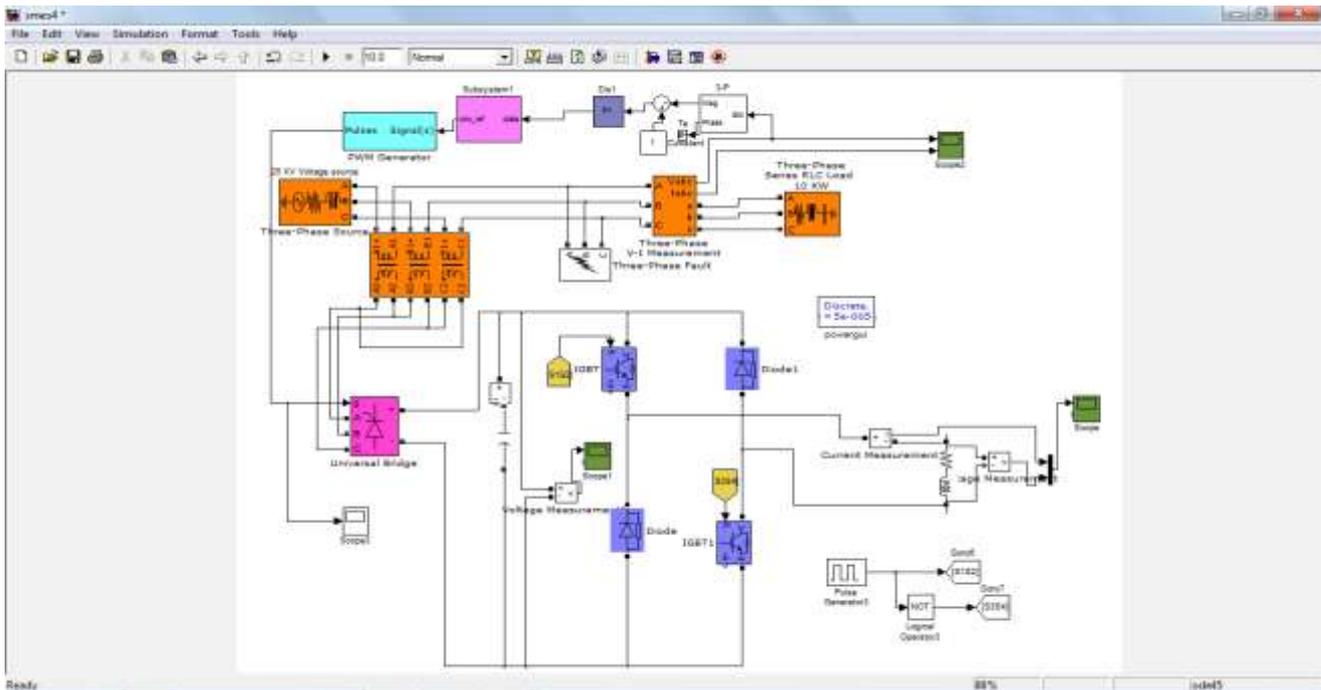


Fig 2.7.2 Simulink model of SMES based DVR system with PI controller

### 2.7.2 DESCRIPTION OF SIMULINK MODEL

Fig 2.7.2 represents a simulink model simulated by injecting a three phase voltage on a three phase source voltage. Load voltage is converted into per unit quantity and is fed to abc to dq converter as shown in simulink model. Then the magnitude is compared with the reference voltage through which error signal is fed to PI controller. The voltage is then fed to triggering circuit PWM ( pulse width modulated ) control technique along with subsystem voltage regulator is applied for inverter switching so as to produce three phase sinusoidal voltage at the load terminals. The PI controller has fault processes the error signal and generates the required angle  $\delta$  to drive the error to zero PI controller input is an actuating signal which is a difference between  $V_{ref}$  and  $V_{in}$ . The controller output compared at PWM signal generator results in desired firing sequence.

The firing of the SMES coil in a VSI inverter of SMES coil during voltage sag through PWM generator having phase angle 0 degree, 120 degree and 240 degree for a set of 6 pulse thyristor. At a time any two thyristor conducts with firing angle 60 degree. At a time Pair of thyristor, T1,T6 or T2,T4 Or T3,T5 conducts in a universal bridge. The controller may also be used to shift the DC-AC inverter into

### 3.0 SIMULATION RESULTS

To validate the proposed technique for implementation of SMES based DVR a MATLAB simulation is carried out. A MATLAB simulation is carried out in following steps for analysis purpose from fig 4.3.2. The first simulation was

rectifier mode to charge the capacitors in the DC energy link in the absence of voltage sags/swells. DC to dc link voltage is held constant through DC-DC chopper in SMES coil.

The dqo transformation or Park's transformation is used to control of DVR. The dqo method gives the sag depth and phase shift information with start and end times. The quantities are expressed as the instantaneous space vectors. Firstly convert the voltage from a-b-c reference frame to d-q-o reference. And then to alpha beta in the voltage regulator. For simplicity zero phase sequence components is ignored. The detection is carried out in each of the three phases. the measured terminal voltage.

When the supply drops below 90% of the reference value voltage sag is observed & voltage swells is detected when supply voltage increases up to 25% of the reference value. The error signal is used as a modulation signal that allows to generate a commutation pattern for the power switches (IGBT's) constituting the voltage source converter. The commutation pattern is generated by means of the pulse width modulation technique (PWM); voltages are controlled through the Modulation The controller output when compared at PWM signal generator results in the desired firing sequence.

done without DVR and a three phase fault is applied to the system at point with fault resistance of 0.001ohm and for a time duration for 0.2-0.7 secs The second simulation is carried out at the same scenario as above but based DVR is now introduced at the load side to compensate the voltage sag occurred due to the three phase fault applied.. The

working of SMES based DVR for voltage compensation at  $0.001\Omega$  fault resistance. The DVR performance in presence of SMES is analysed for symmetrical 3phase fault .

**Step1.** Generation of voltage sag due a three phase fault in the transmission line without SMES based DVR. Triple line to ground fault.

**Step2.** Generation of compensating voltage using  $d - q$  theory.

**Step3.** Implementation of SMES based DVR.

**Step4.** Compensation of voltage sag for type of fault using SMES based technology. Triple line to ground fault.

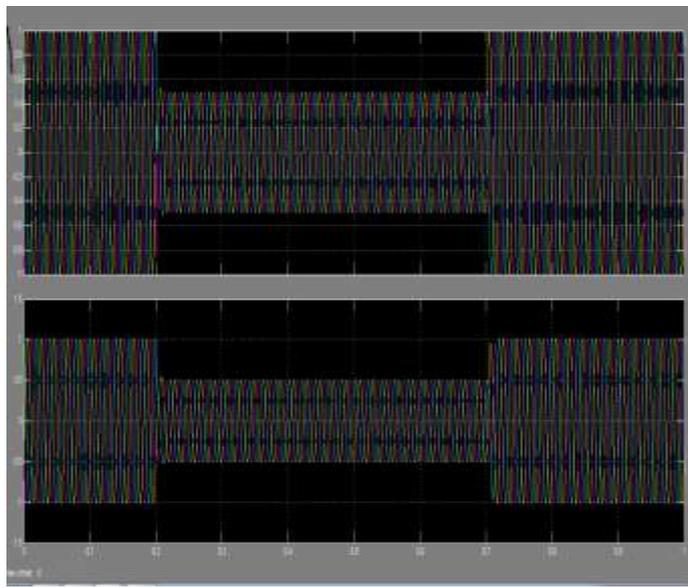


Fig 3.0.1 Simulation of three phase load voltage & current without DVR with three phase fault from fig 2.7.2

Fig3.0.1 represents the compensation without DVR with simulated three phase fault using SMES coil as storage coil. Here transition time of 0.2-0.7 seconds of fault occurrence is taken with fault resistance of 0.001 ohm is taken. On occurrence of 3 phase fault the voltage profile of the system reduced from +1 pu to -1 pu then to 0.5 pu to -0.5 pu . Thus reducing the entire voltage profile to 50% of the fault .On removal of three phase fault voltage profile has recovered as system is able to withstand these transients and able to retain its stability.

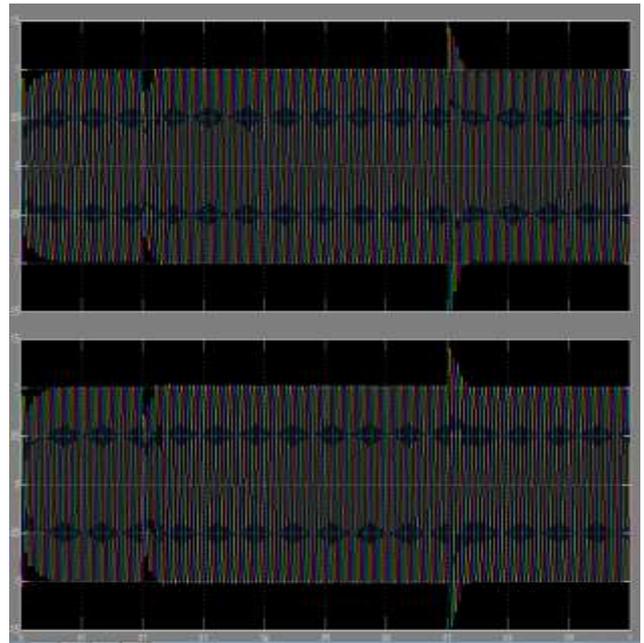


Fig3.0.2 a) Load Voltage With Compensation using DVR with three phase fault from fig 2.7.2in per unit

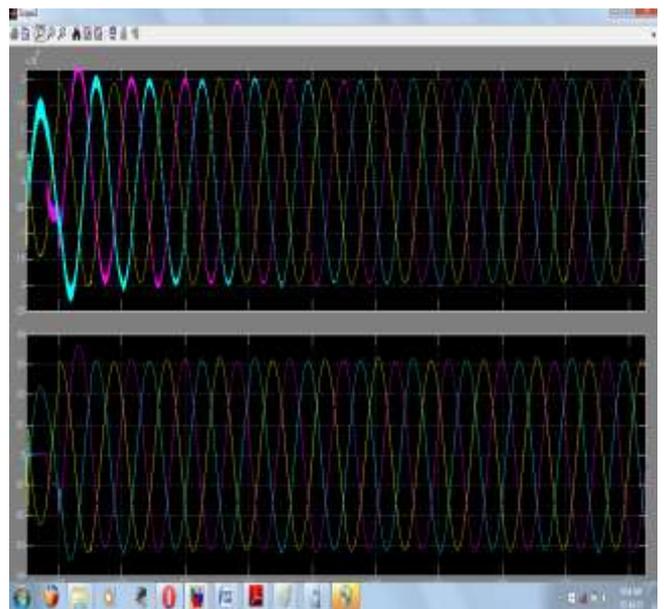


Fig3.0.2 b)Load Voltage With Compensation using DVR with three phase fault from fig 2.7.2

Fig 3.0.2 a)&b) represents the compensation using DVR with simulated three phase fault using SMES coil as storage coil where 3.0.2 a) represents compensation in pu voltage & current & 3.0.2 b) in actual voltage & current with 22kv load voltage. Here transition time of 0.2-0.7 seconds of fault occurrence is taken with fault resistance of 0.001 ohm is taken. On occurrence of 3 phase fault the voltage profile of the system remained to +1 pu to -1 pu .Thus the entire voltage profile remain same to 100% despite of the fault .On removal of three phase fault again voltage profile

recovered as system able to withstand this transients and retain its stability for that time duration again voltage recovered. DVR inject the voltage in place of missing voltage to maintain the entire voltage profile.

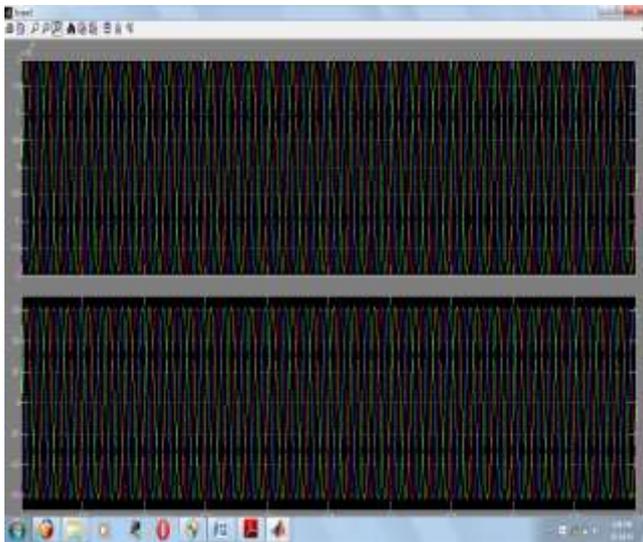


Fig 3.0.3 Compensation with DVR without three phase fault from fig 2.7.2

Fig 3.0.3 represents the compensation using DVR without simulated three phase fault using SMES coil as storage coil. Thus the entire voltage profile remain same to 100% (+22 kv to -22 KV) despite DVR remain in standby mode & the injection voltage in place of missing voltage is zero as the entire voltage profile is maintained.

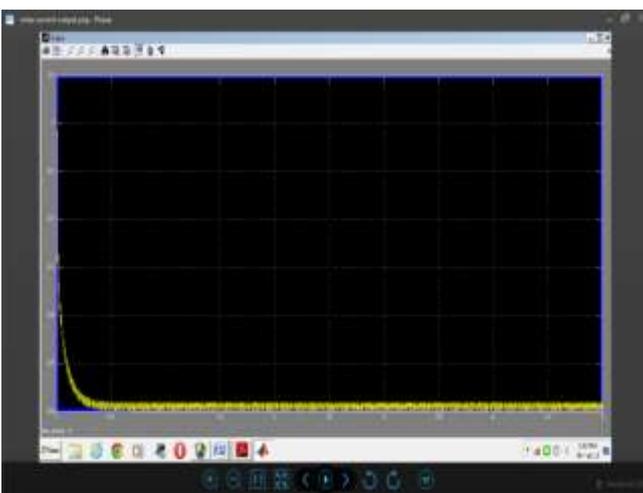


Fig3.0.4 DC Current through SMES coil during standby mode from fig 2.7.2

Fig 3.0.4 Represents the DC current flowing through SMES coil during standby mode. The coil having charging and discharging time as 0.3 sec/0.7 sec is simulation result of fig 2.7.2



Fig 3.0.5 DC Current through SMES coil during standby mode from fig 2.7.2

Fig 3.0.5 Represents the DC current flowing through SMES coil during normal mode. The coil having charging and discharging time as 0.3 sec/0.7 sec

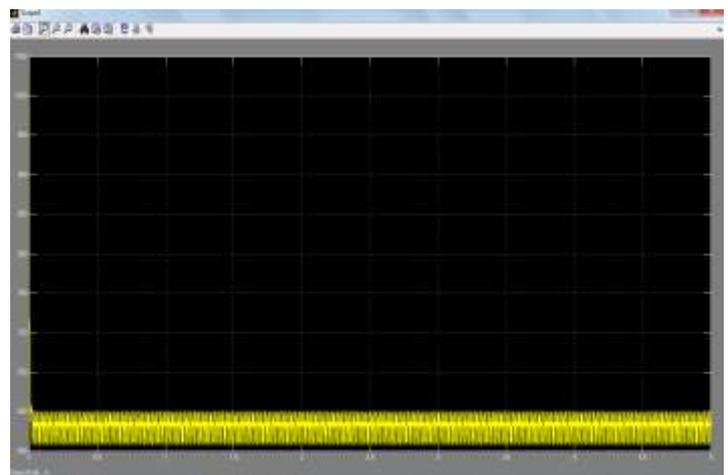


Fig 3.0.6 DC link voltage of SMES coil from fig 2.7.2

Fig 3.0.6 Represents the DC voltage across capacitor. This is the dc link to link voltage feed to dc-dc chopper is basically a pulsating DC converted into pure DC at chopper output terminal.

**TABLE 2. VOLTAGE MAGNITUDE WITH & WITHOUT DVR**

S.NO	TYPE OF SAG	VOLTAGE MAGNITUDE WITH DVR( in percent)	VOLTAGE MAGNITUDE WITHOUT DVR(in percent)

		percent)	
1	3-phase fault	100	50

Table 2 Comparison of voltage magnitude with and without DVR

Three phase fault is considered for the test system delivering linear & non linear load. Here the fault resistance is 0.001 ohm and the ground resistance is 0.001 ohm. The fault is created for the duration of 0.2s to 0.7s. The output waves for the load voltage without and with compensation are shown in Figure 3.0.1(a) & Figure.3.0.1(b). Here it is clear from the output wave shapes that the voltage in the phase where fault is created is increasing during the fault duration in the uncompensated feeder. When DVR is connected in the system the unbalancing is reduced.

#### 4.0 CONCLUSION

A design of superconducting magnetic energy storage module as a dc voltage source to mitigate voltage sags and enhance power quality of a distribution system based on DVR has been presented. The simulation results prove that the SMES based DVR compensate the sags quickly and provide excellent voltage regulation the DVR handles both balanced and unbalanced situation without any difficulties and injects the appropriate voltage. The simulation shows that DVR performance is satisfactory in mitigating voltage sags. DVR has been found to regulate voltage under varying load condition and load unbalancing. The DVR handles fault conditions without any difficulties and injected the appropriate voltage to keep the load voltage balanced and constant at the nominal value. In this study, the DVR has shown the ability to compensate for voltage sags at the distribution side; this can be proved through simulation and experimental results. The efficiency and the effectiveness in voltage sags compensation showed by the DVR makes it an interesting power quality device compared to other custom power devices.

#### 5.0 FUTURE SCOPE

In this dissertation, it is shown that DVR can compensate harmonics in voltage. The work can be expanded in the following area:

1. Other advanced controllers adaptive fuzzy controller, ANFIS, SVPWM Technique can be employed with DVR to increase the effectiveness of DVR in distribution networks.
2. Dynamic loads can be considered in future work and then the effect of DVR can be studied.
3. Wide spread use in power quality improvement.

#### REFERENCE

- [1] Woodley N.H., Senior Member, IEEE, Morgan L., Member, IEEE, Sundaram A., Member, IEEE, "Experience with an Inverter-Based Dynamic Voltage Restorer" IEEE Transaction on Power delivery, Vol. 14, No. 3, July 1999.
- [2] Chang C.S., Ho Y.S., "The Influence of Motor Loads on the Voltage Restoration Capability of the Dynamic Voltage Restorer" PowerCon, International Conference, Vol. 2, pp. 637-642, 2000.
- [3] Zhan Changjiang, Ramachandaramurthy Vigna, Arulampalam Atputharajah, Fitzner Chris, Kromlidis Stylianos, Barnes Mike, Jenkins Nicholas, "Dynamic Voltage Restorer Based on Voltage SVPWM Control" IEEE Transactions on Industry Applications, Vol. 37, No. 6, Nov-Dec. 2001.
- [4] Godsk Nielsen John., Blaabjerg Frede, "Control Strategies for Dynamic Voltage Restorer Compensating Voltage Sags with Phase Jump" Applied Power Electronics Conference and Exposition, IEEE, Vol. 2, pp. 1267-1273, 2001.
- [5] Chellali Benachaiba, Brahim Ferdi, "Voltage Quality Improvement Using DVR" *Electrical Power Quality and Utilisation*, Vol. 14, No. 1, pp. 39-46, 2008.
- [6] Pal Yash, Swarup A., Senior Member, IEEE, Singh Bhim, Senior Member, IEEE "A Review of Compensating Type Custom Power Devices for Power Quality Improvement" *IEEE Power India Conference*, pp. 1-8, 2008.
- [7] M. Manikandan,"Power quality compensation using SMES coil with FLC" International Journal of Advances in Engineering & Technology, Sept. 2013, ISSN: 22311963
- [8] Himali. S. Chaudhari, Deepak. P. Kadam " Voltage Sag Mitigation Using Smes Based DVR Technology", International journal of scientific & technology research Volume 3, Issue 3, March 2014 ISSN 2277-8616
- [9] Omar Rosli, Rahim N.A., Sulaiman Marizad, "Dynamic Voltage Restorer Application for Power Quality Improvement in Electrical Distribution System: An Overview" *Australian Journal of Basic and Applied Sciences*, pp. 379-396, 2011.
- [10] Md.Azim Riyasat, Md.Hoque Ashraful, Dept. of EEE, Islamic University of Technology, Boardbazar, Gazipur, Bangladesh, "A Fuzzy Logic Based DVR for Voltage Sag and Swell Mitigation for Industrial Induction Motor Loads" *International Journal of Computer Applications*, Vol. 30, No. 8, Sept- 2011.
- [11] Ayson Basa Arsoya, R. M. Bucci, and K. T. Sweden,"Improved Superconducting Magnetic energy

- Storage Controller” IEEE Conference on Power Electronics Specialists Jun 1991 page no: 338-344.
- [12] Improving power delivery through the application of superconducting magnetic energy storage (SMES), S. Ashwnath Member, IEEE, Pedro Enrique Mercado, Senior Member, IEEE, and Edson Hirokazu Watanabe, Senior Member, IEEE, 2009, pp 228-267
- [13] D Harikrishna, and K. T. Sweden, “400-MW SMES power conditioning system development and simulation,” IEEE Trans. Power Electron., vol. 8, no. 3, pp. 237–249, Jul. 1993
- [14] Improved Superconducting Magnetic Energy Storage (SMES) Controller for High-Power Applications. Marcelo Gustavo Molina, Member, IEEE, Pedro Enrique Mercado, Senior Member, IEEE, and Edson Hirokazu Watanabe, Senior Member, IEEE, 2007, pp 228-258
- [15] Vidya bhusan kumar-Bin d Roger A. Douga,, “An Overview of SMES Applications in Power and Energy Systems,” IEEE Transactions on Sustainable Energy, vol. 1, no. 1, April 2010
- [16] Anil kumar Dahiya “0-MJ Superconducting Magnetic Energy Storage System for Electric Utility Transmission Stabilization”, Proc. IEEE, Vol. 71, NO. 9, pp 1099-1107, 1983
- [17] D. Sutanto, M. V. Aware. Improved controller for high temperature super conducting magnetic energy storage (HTS-SMES). 2009 IEEE Intel. Conf. Appl. Supercond. and Electromag. Dev., 2009: 361-364.
- [18] M.A. Daugherty, W. E. Budkles, F. A. Knudtson, D. L. Mann, and P.W. Stephensoo, "SSD operating experience," IEEE Trans. Applied Superconductivity, vol. 3, no. 1, pp. 204-206, March 1993.
- [19] Jurado Francisco, Member, IEEE, Manuel Valverde, University of Jaen, “Fuzzy Logic Control of a Dynamic Voltage Restorer” IEEE-ISIE, Vol. 2, pp. 1047-1052, 2004.
- [20] Boonchiaml Paisan, Mithulananthan Nadarajah, Rajamangala University of Technology, Thanyaburi, Thailand, “Detailed Analysis of Load Voltage Compensation for Dynamic Voltage Restorers” TENCON, IEEE region 10 conference, pp. 1-4, 2006.
- [21] Jowder Al. Fawzi, “Modeling and Simulation of Dynamic Voltage Restorer Based on Hysteresis Voltage Control” 33rd Annual Conference of the IEEE Industrial Electronics Society (IECON), Nov. 5-8, 2007, Taipei, Taiwan
- [22] Kumar Ravi, Nagaraju Siva, J.N.T.U. College of Engineering, Kakinada, A.P, India, “Simulation of D-STATCOM and DVR in Power” APRN Journal of Engineering and Applied Sciences, ISSN 1819-6608, Vol. 2, No. 3, June 2007.
- [23] .Ashari M., Hiyama T., Pujiantara M., Suryoatmojo H., Purnomo M.H., “A Novel Dynamic Voltage Restorer with Outage Handling Capability Using Fuzzy Logic Controller” Innovative Computing, Information and Control, pp.51, 5-7 Sept. 2007.
- [24] Margo P., Heri P.M., Ashari M., Hendrik M., Hiyama T., “Compensation of Balanced and Unbalanced Voltage Sags using DVR Based on Fuzzy Polar Controller” International Journal of Applied Engineering Research, ISSN 0973-4562, Vol. 3, No. 3, pp. 879-890, 2008.
- [25] JayaPrakash P., Member, IEEE, Singh bhim, Senior Member, IEEE, Kothari D.P., Senior Member, IEEE, Chandra Ambrish, Senior Member, IEEE, Al-Haddad Kamal, Fellow, IEEE, “Control of Reduced Rating DVR with Battery ESS” IEEE, 2008.
- [26] Chellali Benachaiba, Brahim Ferdi, “Voltage Quality Improvement Using DVR” Electrical Power Quality and Utilisation, Vol. 14, No. 1, pp. 39-46, 2008.
- [27] Pal Yash, Swarup A., Senior Member, IEEE, Singh Bhim, Senior Member, IEEE “A Review of Compensating Type Custom Power Devices for Power Quality Improvement” IEEE Power India Conference, pp. 1-8, 2008.
- [28] El. Shennawy Tarek, El-Kamar Wady, El-Gammal Mahmoud, Abou-Ghazala Amr, Monem Moussa Abdel, Electrical Power Engineering Dept., Faculty of Engineering, Alexandria University, Egypt, “Mitigation of Voltage Sags in a Refinery with Induction Motors Using DVR” European Journal of Scientific Research, ISSN 1450-216X, Vol. 36, No. 1, pp. 118-131, 2009.
- [29] Ezoji H., Sheikholeslami A., Tabasi., Saeednia M.M., Electrical & Computer Engineering Department, Babol University of Technology, “Mitigation of DVR Using Hysteresis Voltage Control” European Journal of Scientific Research, ISSN 1450-216X, Vol. 27, No. 1, pp. 152-166, 2009.
- [30] Ferdi B., Dib S., Dehini R., University of Bechar, Algeria “Adaptive PI Control of Dynamic Voltage Restorer Using Fuzzy Logic” Journal of Electrical Engineering: Theory and Application Vol.1, pp. 165-173, 2010.
- [31] Katole D.N., Research scholar: Department of Electrical Engg, G.H. Raisoni College of engineering, Nagpur, Maharashtra, India, “Analysis and Mitigation of Balanced Voltage Sag with the Help of Energy Storage System” ICETET, pp. 317-321, 2010.
- [32] Teke A., Bayindir K., Tumay M., Department of Electrical and Electronics Engineering, Cukurova University, Adana, Turkey, “Fast sag/swell detection method for fuzzy logic controlled dynamic voltage restorer” generation transmission and distribution IET, Vol. 4, pp. 1-12, 2010.
- [33] Gupta Sunil, “Dynamic Voltage Restorer against Voltage Sag” International Journal of Innovation, Management and Technology, Vol. 1, No. 3, pp. 232-237, 2010.
- [34] Md.Azim Riyasat, Md.Hoque Ashraful, Dept. of EEE, Islamic University of Technology, Boardbazar, Gazipur, Bangladesh, “A Fuzzy Logic Based DVR for Voltage Sag and Swell Mitigation for Industrial Induction Motor Loads” International Journal of Computer Applications, Vol. 30, No. 8, Sept- 2011.
- [35] Chawla Puneet, Khanna Rintu, Singh Abinash, “Fuzzy Logic Control for DVR to Counter Voltage Sag on a Distribution Network” Proceedings of the World Congress on Engineering, London, Vol. 2, July 2011.
- [36] Khanh Q. Bach, Lian, J., Ramachandran, B., Srivastava, S., Cartes, D., “Mitigating Voltage Sags due to DOL Starting of Three Phase Asynchronous Motors using Dynamic Voltage Restorer” IEEE, 2011.

- [37] Kanakaraj M., Thirukkovai S., "Voltage Sag/Swell Compensation using Fuel Cell Fed Inverter Based Dynamic Voltage Restorer" *Advances in Engineering, Science and Management (ICAESM), International Conference*, pp.176,182, 30-31 March 2012.
- [38] Jain Sandesh, Thakur Shivendra, Phulambrikar S.P., "Fuzzy Controller Based DVR to Mitigate Power Quality and reduce the Harmonics Distortion of Sensitive Load" *International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering*, Vol. 1, Issue 5, pp. 351-361.
- [39] M. H. Ali, T. Murata, and J. Tamura, "Transient stability enhancement by fuzzy logic-controlled SMES considering coordination with optimal reclosing of circuit breakers," *IEEE Trans. Power Syst.*, vol. 23, no. 2, pp. 631–640, May 2008.
- [40] Omar Rosli, Rahim N.A., Sulaiman Marizad, "Dynamic Voltage Restorer Application for Power Quality Improvement in Electrical Distribution System: An Overview" *Australian Journal of Basic and Applied Sciences*, pp. 379-396, 2011
- [41] W. V. Torre and S. Eckroad, "Improving power delivery through the application of superconducting magnetic energy storage (SMES)," in Proc. IEEE Power Eng. Soc. Winter Meeting, vol. 1, 2001, pp. 81–87.
- [42] I. D. Hassan, R. M. Bucci, and K. T. Swe, "400 MW SMES power conditioning system part-1 performance requirement and configuration," IEEE Conference on Power Electronics Specialists Jun 1991 page no: 338-344.
- [43] D. Casadei, G. Grandi, U. Reggiani, and G. Serra, "Analysis of a power conditioning system for superconducting magnetic energy storage (SMES)," in Proc. IEEE Int. Symp. Industrial Electronics (ISIE'98), vol. 2, 1998, pp. 546–551.
- [44] Mohd. Hasan Ali, Bin Wu and Roger A. Dougal, "An Overview of SMES Applications in Power and Energy Systems," *IEEE Transactions on Sustainable Energy*, vol. 1, no. 1, April 2010
- [45] Q. Yao and D. G. Holmes, "A simple, novel method for variable-hysteresis- band current control of a three-phase inverter with constant switching frequency," in Proc. Industry Applications Soc. Annu. Meeting, vol. 2, 1993, pp. 1122–1129.
- [46] M. Lafoz, I. J. Iglesias, C. Vezanones, and M. Visiers, "A novel double hysteresis band control for a three-level voltage source inverter," in Proc. IEEE 31st Ann. Power Electronics Specialists Conf. (PESC), vol. 1, 2000, pp. 21–26.
- [47] O. L. Gyugyi, E.C. Strycula, "Active AC Power Filter," Proc. IEEE-IAS
- [48] H. Akagi, Y. Kanazawa, A. Nabae, "Instantaneous Reactive Power Annual Meeting, pp. 529, 1976. Compensators Comprising Switching Devices without Energy Storage Components," *IEEE Trans. on IA*, Vol. 20, pp. 625, 1984.
- [49] L. Malesani, L. Rossetto, P. Tenti, "Active Filters for Reactive Power and Harmonic Compensation," Proc. IEEE-PESC, pp. 321-330, June 1986.
- [50] Simon, H. Spaeth, K.P. Juengst, P. Komarek, "Experimental Setup of a Shunt Active Filter Using a Superconducting Magnetic Energy Storage Device," Proc. EPE97, Vol. 1, pp. 447-452, September 1997.
- [51] R W Boom and H A Peterson, "Superconductive Energy Storage for Power Systems". *IEEE Trans. Magnetics*, MAG-8, No. 3, pp 701 -703, 1972
- [52] J D Rogers et al. "30-MJ Superconducting Magnetic Energy Storage System for Electric Utility Transmission Stabilization", Proc. IEEE, Vol. 71, NO. 9, pp 1099-1107. 1983
- [53] SMSchoenung, W. V. Hassenzahl and P. G. Filios, "US Program to Develop Superconducting Magnetic Energy Storage." Proc. of 23rd Inter Society Energy Conversion Engineering Conference Vol. 2, pp 537440, 1988.
- [54] D. Casadei, G. Grandi, U. Reggiani, and G. Serra, "Analysis of a power conditioning system for superconducting magnetic energy storage (SMES)," in Proc. IEEE Int. Symp. Industrial Electronics (ISIE'98), vol. 2, 1998, pp. 546–551.
- [55] B. K. Bose "Modern power electronics and AC Drives", Prentice Hall, 2002.
- [56] Muhammad. Rashid, "Power electronics circuits, device, and applications second edition", Prentice-Hall International Editions, 1993, pp 382-500
- [57] N. Mohan, T. Underland and W. Robbins, "Power Electronics: Converters, Applications, and design", John Wiley & Sons, 3rd edition, 2003.